Decode\_And\_Execute:

Carry\_Look\_Ahead\_Adder\_8bit:

- Please provid more explantation about youe desgin instead of only showing the circuit diagram

- There is no explaination about the benefit of CLA

Exhausted\_Testing:

FPGA:

Take Away Part:

- It is great that you have learned how to design using universal gates as well as seven segment display.

- Backing up data on the cloud is a good habbit during the course of your undergraduate and probably even more important in the graduate studies.